



# RISE

RISC-V Software Ecosystem

## Prime Guide to the RISE Project

For Potential Sponsors

# RISE (RISC-V Software Ecosystem)

## Objective

- Raise the quality of RISC-V Platform implementations to match to other architectures.
- Push the RISC-V Software ecosystem forward and align ecosystem partners' efforts.
- Ensure RISC-V is a tier 1 platform for all tools and libraries.
- Accelerating RISC-V Adoption for Client and above segments.

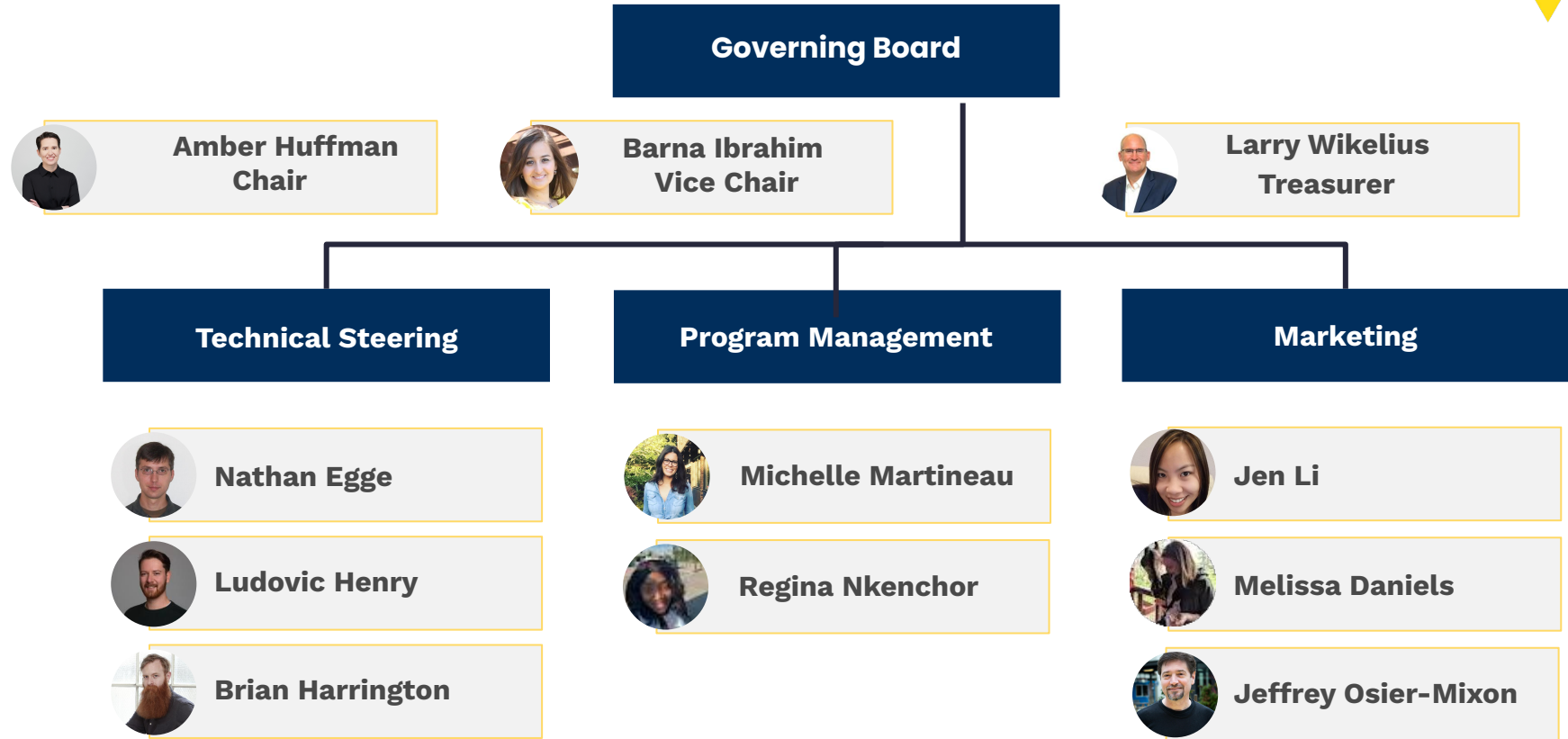
### Premier Members



### General Members



# RISE Governance Organization Chart



# RISE Working Groups

Areas the RISE TSC targets to focus on over time:

Working Group	Confluence
System Libraries	<a href="https://wiki.riseproject.dev/display/HOME/System+Libraries+WG">https://wiki.riseproject.dev/display/HOME/System+Libraries+WG</a>
Simulator / Emulator	<a href="https://lf-rise.atlassian.net/wiki/spaces/HOME/pages/8588790/Simulator+Emulator+WG">https://lf-rise.atlassian.net/wiki/spaces/HOME/pages/8588790/Simulator+Emulator+WG</a>
Language Runtimes	<a href="https://wiki.riseproject.dev/display/HOME/Language+Runtimes+WG">https://wiki.riseproject.dev/display/HOME/Language+Runtimes+WG</a>
Kernel KVM	<a href="https://wiki.riseproject.dev/display/HOME/Kernel+and+Virtualization+WG">https://wiki.riseproject.dev/display/HOME/Kernel+and+Virtualization+WG</a>
Firmware	<a href="https://lf-rise.atlassian.net/wiki/spaces/HOME/pages/8587319/Firmware+WG">https://lf-rise.atlassian.net/wiki/spaces/HOME/pages/8587319/Firmware+WG</a>
Distro Integration	<a href="https://lf-rise.atlassian.net/wiki/spaces/HOME/pages/8586089/Distro+Integration+WG">https://lf-rise.atlassian.net/wiki/spaces/HOME/pages/8586089/Distro+Integration+WG</a>
Compilers and Toolchains	<a href="https://wiki.riseproject.dev/display/HOME/Compilers+and+Toolchains+WG">https://wiki.riseproject.dev/display/HOME/Compilers+and+Toolchains+WG</a>
Security Software	<a href="https://lf-rise.atlassian.net/wiki/spaces/HOME/pages/8591245/Security+Software+WG">https://lf-rise.atlassian.net/wiki/spaces/HOME/pages/8591245/Security+Software+WG</a>
Developer Infrastructure	<a href="https://lf-rise.atlassian.net/wiki/spaces/HOME/pages/8585662/Developer+Infrastructure+WG">https://lf-rise.atlassian.net/wiki/spaces/HOME/pages/8585662/Developer+Infrastructure+WG</a>
AI/ML	<a href="https://lf-rise.atlassian.net/wiki/spaces/HOME/pages/349372433/AI+ML+Working+Group">https://lf-rise.atlassian.net/wiki/spaces/HOME/pages/349372433/AI+ML+Working+Group</a>

# Developer Appreciation Program



## Program Details

Running from October 2024 to December 2025, the program offers up to €50,000 in rewards. Developers who port, test, and release open-source projects on RISC-V are eligible to participate, with two reward tiers:

- Small Projects: Minimal porting, such as adding linux-riscv64 support in CI pipelines.
  - Reward: €500.
- Large Projects: Significant porting, such as incorporating a RISC-V backend and updating testing pipelines.
  - Reward: €3,000.

## Focus on Open Source

This initiative is dedicated to open-source contributions. All contributions must be publicly available and licensed under an open-source license. By prioritizing the enablement of projects over pure performance optimization, we aim to ensure that RISC-V becomes a ubiquitous platform in the software world.

For larger, more complex projects that require weeks or months of work, we encourage developers to apply for support through the [RFP program](#), which is better suited for large-scale efforts.



# RISE RFP Program

RISE is dedicated to enhancing the software ecosystem for RISC-V by collaborating with upstream open-source projects to support the RISC-V architecture. While companies supporting RISE contribute to these projects, they may not always have the necessary expertise, and the Request for Proposal (RFP) process helps extend RISE's impact by allowing others to take on identified work. This process is designed to be open and transparent, aligning with the open-source community values.

## **The RFP lifecycle stages**

### **Proposal**

identifying the need, socializing the proposal within RISE for support, reviewing and approving it through the Technical Steering Committee (TSC) and Governing Board (GB), and then publishing the proposal for bidding.

### **Bidding**

Contractors submit their proposals via a public bidding process, which is reviewed by a designated team, and after final approval, a contract is awarded.

### **Execution**

The execution phase involves managing contractors, setting milestones, and providing regular updates. Finally, invoicing and concluding the work are handled with clear procedures for transparency and accountability.



# Membership

Thank you for your interest in joining RISE, we're excited to collaborate with you! RISE has two membership levels listed below, currently, only the General membership is available because the Governing Board is at capacity. Membership to both Linux Foundation Europe and RISC-V International are a prerequisite to acceptance to the RISE Project.

## **RISE Sponsorship Levels:**

Premier: EUR 80,000 ( Includes Governing Board Seat)

General: EUR 40,000

[Join RISE Now](#)

## **Linux Foundation Europe Sponsorship Information.**

Your organization will need to be a current Sponsor of LFEU. If your organization is already a Sponsor of the LFEU, there is no need to do anything. If you are not a Sponsor of the LFEU, there are three tiers of LFEU sponsorship available. The fees associated with each level of LFEU sponsorship are included below for easy reference. Please visit the Sponsorship page at the [LFEU website for full details](#).

- LFEU Platinum: EUR 500,000
- LFEU Gold: EUR 100,000
- LFEU Silver: Under 100 employees: EUR 5,000; 100-499 employees: EUR 10,000; 500-4,999 employees: EUR 15,000; 5,000 or more employees: EUR 20,000.
- LFEU Associate sponsorship is available for non-profit, academic, and government entities at no cost.

If you have a LF membership you may join the LFEU without a fee, you will just apply at the same level you currently hold.

## **RISC-V International Membership Information**

Your organization will also need to be a current Sponsor of RISC-V international. If your organization is already a Sponsor of RVI, there is no need to do anything. If you are not a Sponsor of RVI, please visit the [RVI website for full details](#).

# Quick Links

[Wiki](#)

[Website](#)

Questions? [info@riseproject.dev](mailto:info@riseproject.dev)